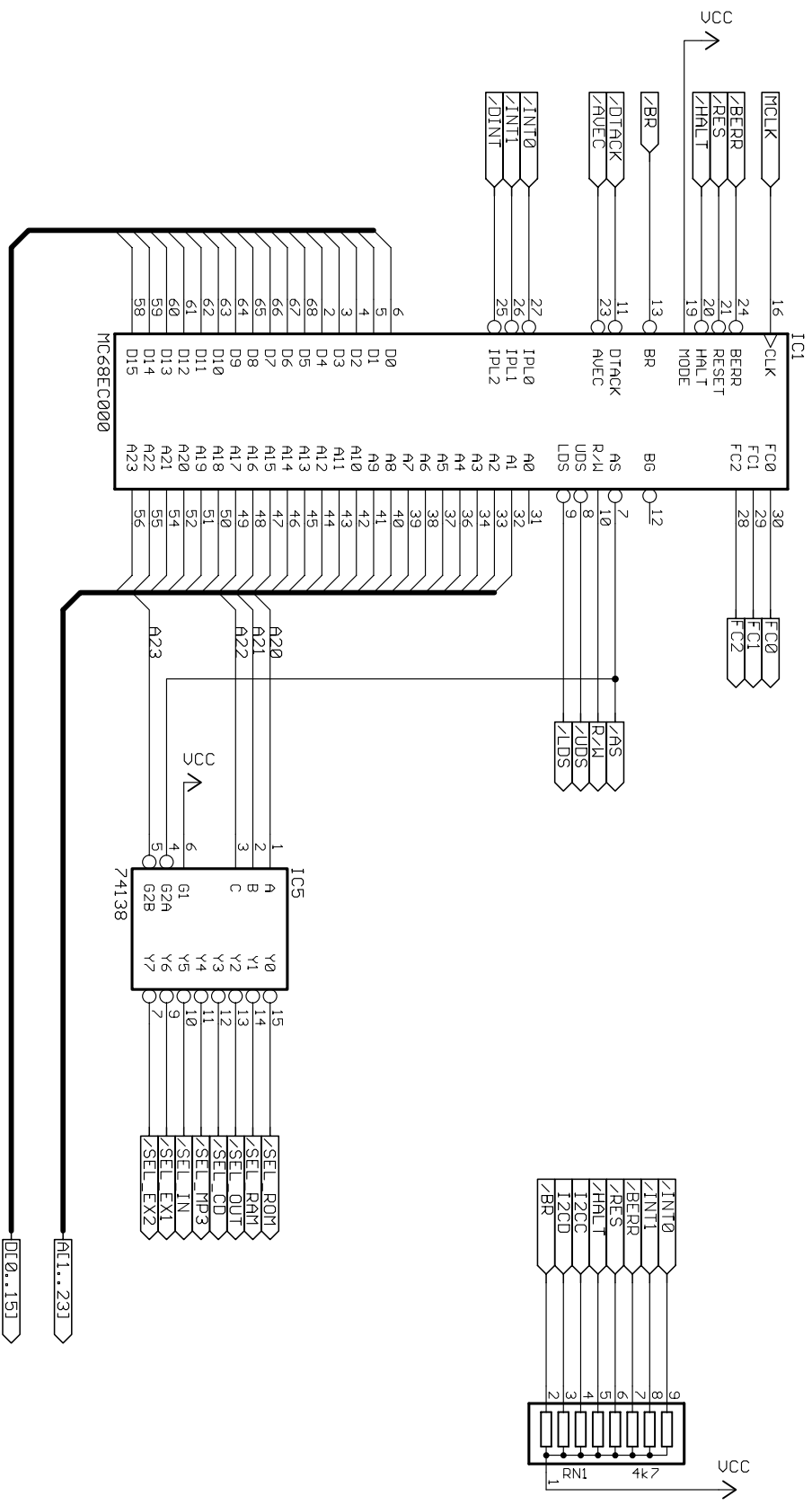


# CPU



# CPU

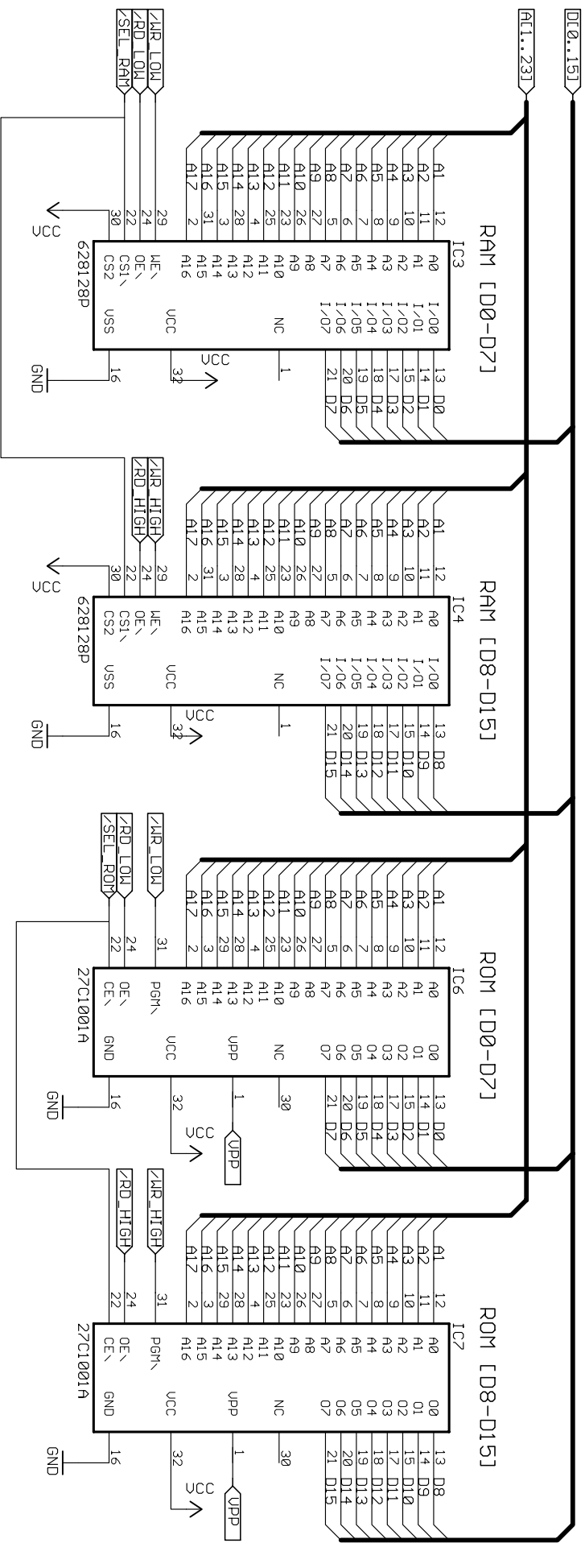
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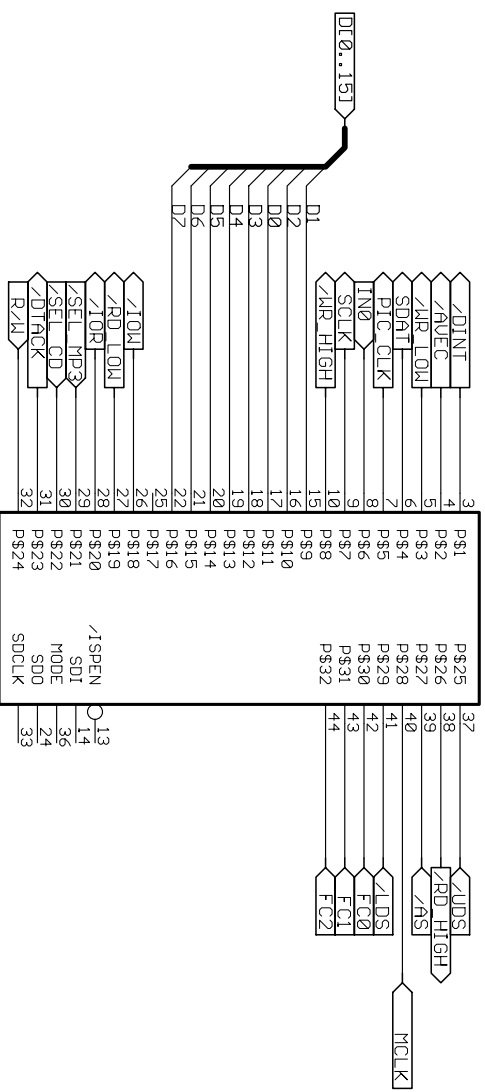
Date: 17. 04. 2000 08:57:00

Sheet: 1/1

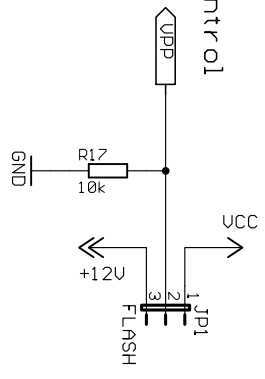


Addr. Decoder

US146



Write Control



RAM, ROM . . .

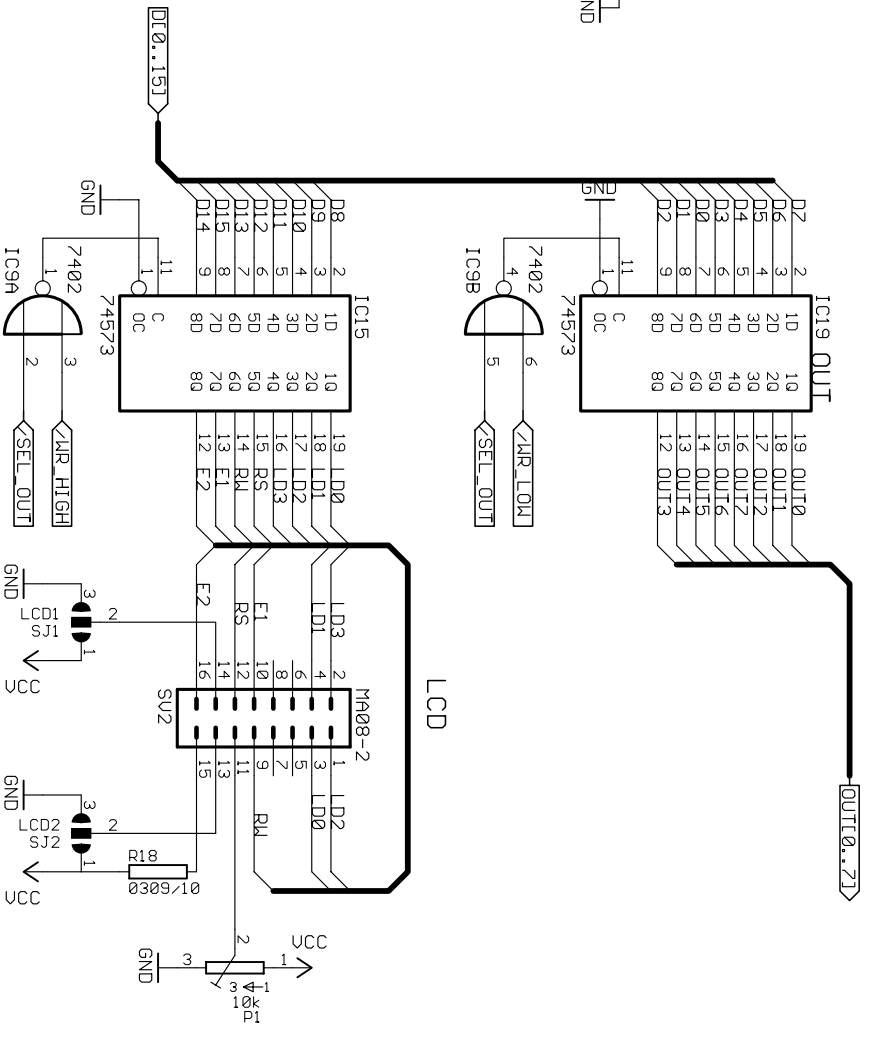
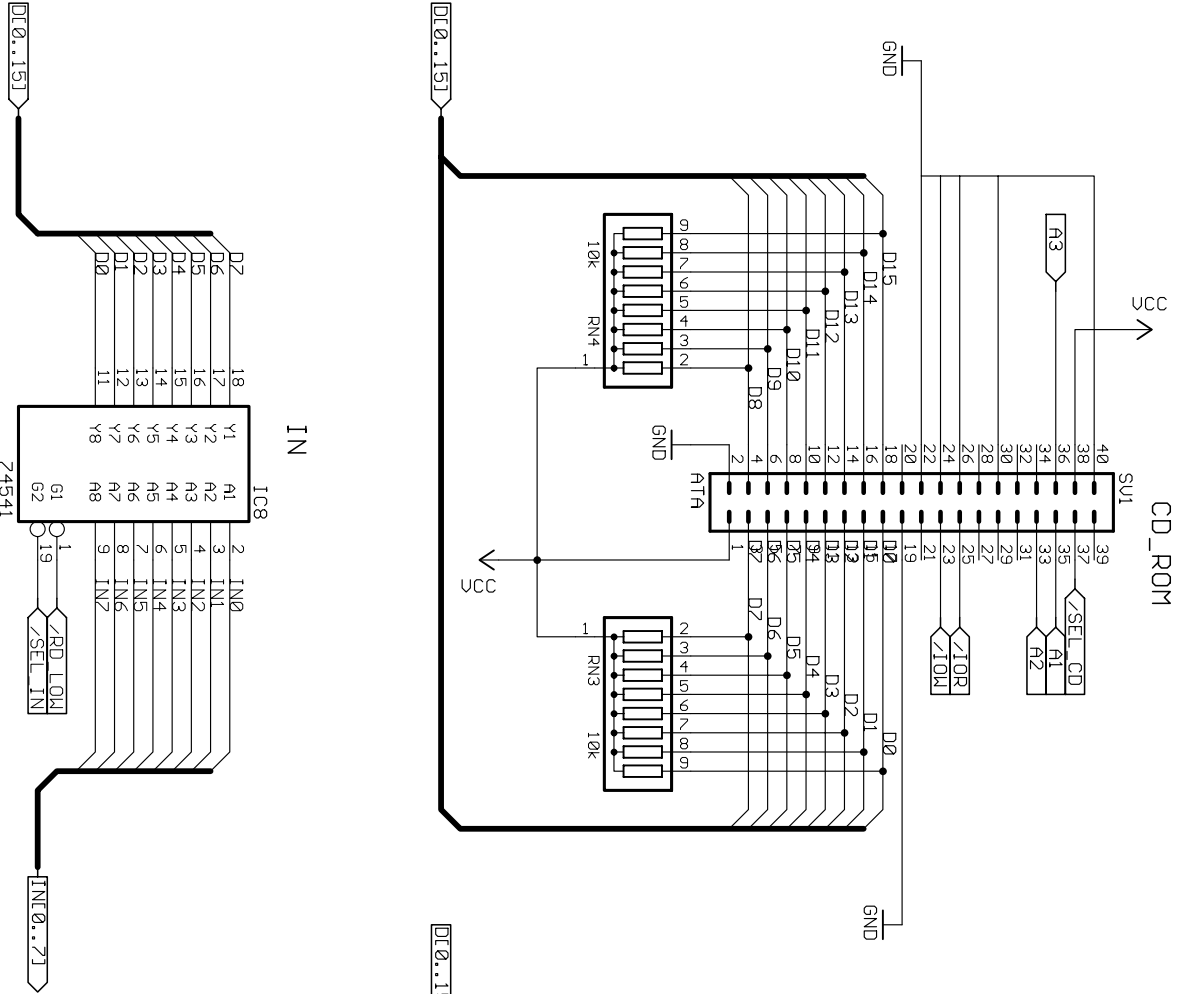
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# CD-ROM, LCD

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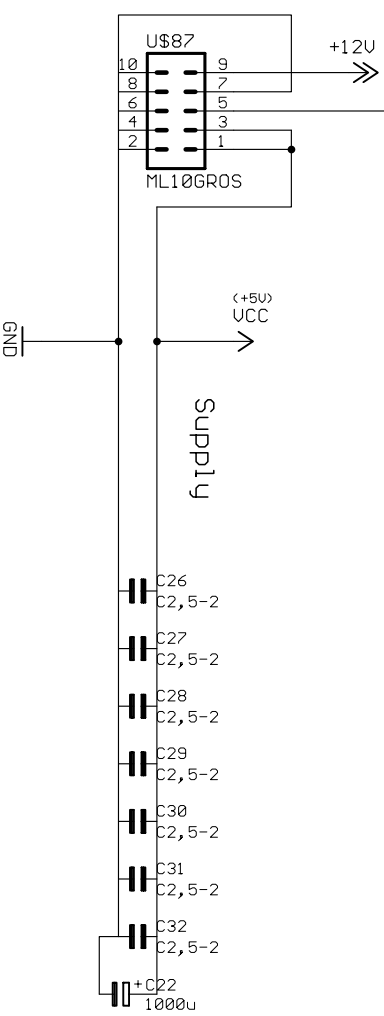
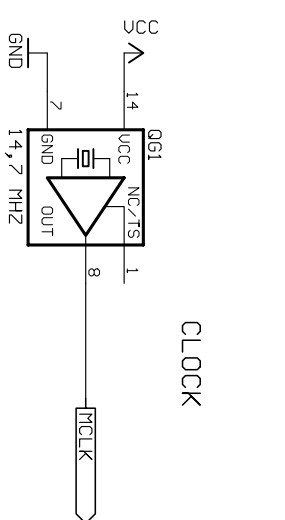
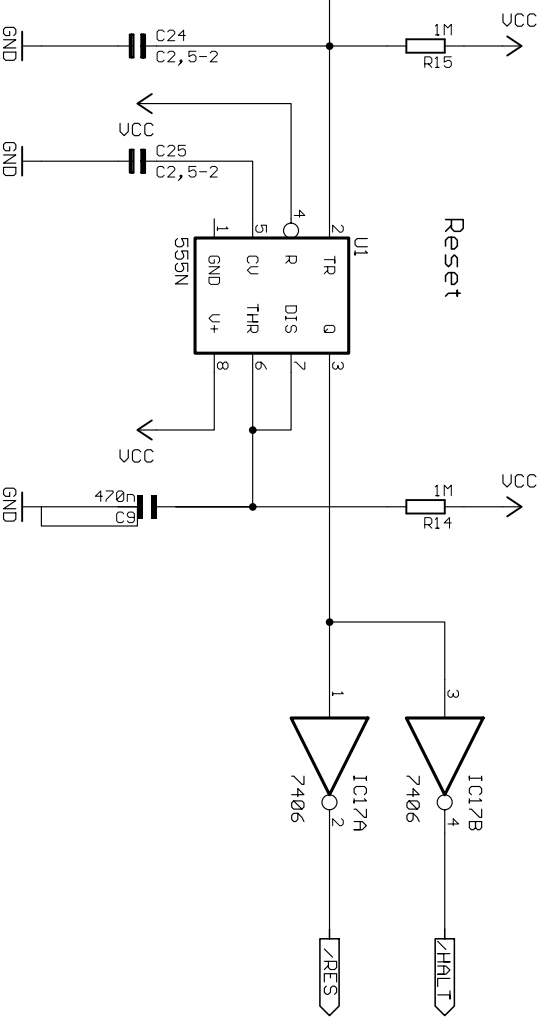
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Sheet: 1/1





# Reset, Clk, Exp.

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